

RCE filed
02.06.06



Response under 37 CFR 1.116
Expedited Procedure
Examining Group 2823

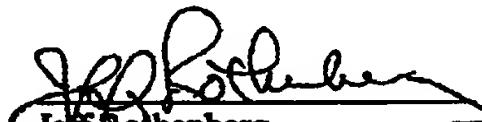
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Grutzendieck et al.	Confirmation No.:	4906
Serial No.:	09/806,224	Examiner:	George Fourson
Filed:	06/11/2001	Group Art Unit:	2823
Title:	Method for Producing Transistors		

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being transmitted by facsimile to Examiner George Fourson at 571-273-8300 in the United States Patent and Trademark Office on December 2, 2005.


 Jeff Rothenberg
 Attorney for Applicants
 Reg. No. 26,429

Date of Signature: 12/2/2005

Fax No.: 571-273-8300
 No. of Pages Transmitted: 9

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

AMENDMENT AFTER FINAL REJECTION

Dear Sir:

In response to the Office Action dated September 2, 2005, kindly amend the above-identified application as follows: